

BSI Standards Publication

Performance of high-voltage direct current (HVDC) systems with line-commutated converters

Part 1: Steady-state conditions



National foreword

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TECHNICAL REPORT



Performance of high-voltage direct current (HVDC) systems with line-commutated converters –

Part 1: Steady-state conditions

INTERNATIONAL ELECTROTECHNICAL COMMISSION

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FOREWORD

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IEC 60919-1, which is a technical report, has been prepared by subcommittee 22F: Power electronics for electrical transmission and distribution systems, of IEC technical committee 22: Power electronic systems and equipment.

This edition includes the following significant technical changes with respect to the previous edition:

- a) the changes have been made to the description of multi 12-pulse groups per pole, especially for a large scale ultra high-voltage direct current (UHVDC) converter arrangement;
- b) the different arrangements of d.c. smoothing reactors have been included;

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c) the figures depicting two 12-pulse groups per pole arrangement have been added.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

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The committee has decided that the contents of the base publication and its amendments will remain unchanged until the stability date indicated on the IEC web site under "http://webstore.iec.ch" in the data related to the specific publication. At this date, the publication will be

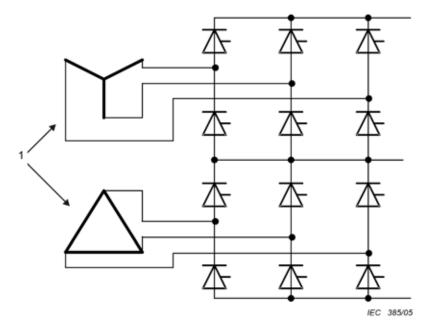
- · reconfirmed,
- · withdrawn,
- replaced by a revised edition, or
- amended.

Performance of high-voltage direct current (HVDC) systems with line-commutated converters —

Part 1: **Steady-state conditions**

1 Scope

This part of the IEC 60919 provides general guidance on the steady-state performance requirements of high-voltage direct current (HVDC) systems. It concerns the steady-state performance of two-terminal HVDC systems utilizing 12-pulse converter units comprised of three-phase bridge (doubleway) connections (see Figure 1), but it does not cover multi-terminal HVDC transmission systems. Both terminals are assumed to use thyristor valves as the main semiconductor valves and to have power flow capability in both directions. Diode valves are not considered in this report.



Key

1 Transformer valve windings

Figure 1 — Twelve-pulse converter unit

Only line-commutated converters are covered in this report, which includes capacitor commutated converter circuit configurations. General requirements for semiconductor line-commutated converters are given in IEC 60146-1-1, IEC/TR 60146-1-2 and IEC 60146-1-3. Voltage-sourced converters are not considered.

This technical report, which covers steady-state performance, is followed by additional documents on dynamic performance and transient performance. All three aspects should be considered when preparing two-terminal HVDC system specifications.

The difference between system performance specifications and equipment design specifications for individual components of a system should be realized. Equipment specifications and testing requirements are not defined in this report. Also excluded from this report are detailed seismic